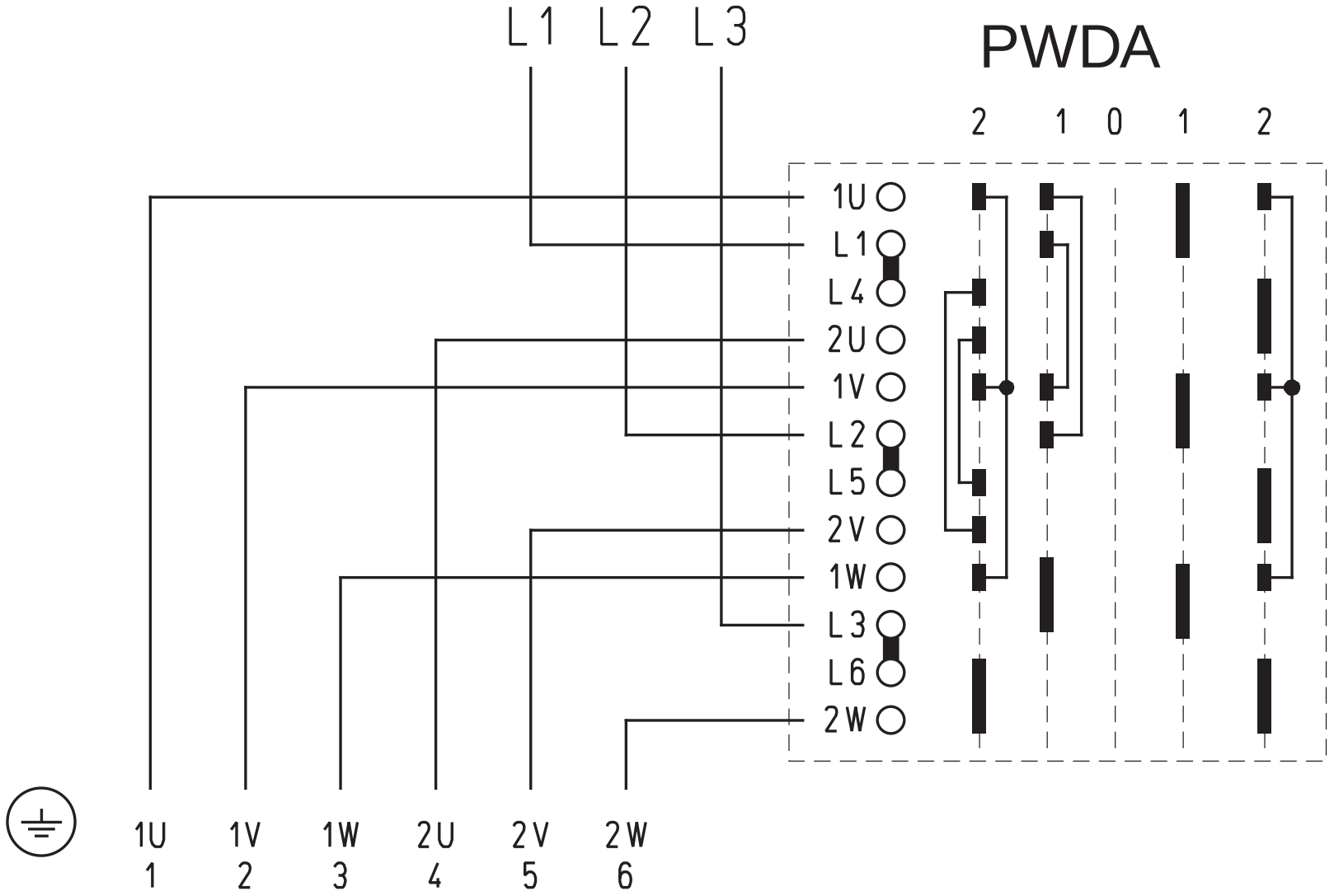

L1 L2 L3
PWDA
2 1 0 1 2
1U
L1
L4
2U
1V
L2
L5
2V
1W
L3
L6
2W
1U 1V 1W 2U 2V 2W
1 2 3 4 5 6

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